

Electronic Acknowledgement Receipt

EFS ID:	1098262
Application Number:	10699764
Confirmation Number:	4234
Title of Invention:	In-circuit configuration architecture for embedded configurable logic array
First Named Inventor:	Albert Sun
Customer Number:	22470
Filer:	Diane M. Bergin
Filer Authorized By:	
Attorney Docket Number:	MXIC 1520-1
Receipt Date:	29-JUN-2006
Filing Date:	03-NOV-2003
Time Stamp:	18:08:05
Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		efs116AMDMT.pdf	359852	yes	11

	Multipart Description		
	Doc Desc	Start	End
	Amendment After Final	1	1
	Claims	2	6
	Applicant Arguments/Remarks Made in an Amendment	7	11

Warnings:

Information:

2	Information Disclosure Statement (IDS) Filed	efsIDS.pdf	723865	no	4
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Warnings:

Information:

3	Foreign Reference	WO0052825A1.pdf	1781724	no	51
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Warnings:

Information:

4	Foreign Reference	efsJP668320.pdf	562966	no	9
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Warnings:

Information:

5	Foreign Reference	efsNikkei.pdf	147552	no	3
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Warnings:

Information:

Total Files Size (in bytes):			3575959		
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.